

**IN THE SPECIFICATION:**

Please amend paragraph [0033] as follows:

[0033] FIG. [[6]] 6A is a schematic cross-sectional view, which is taken along [[a]] line “VI-VI” of FIG. 5, and FIG. 6B is a schematic plane view showing an array layer of an organic electroluminescent device according to an embodiment of the present invention;

Please amend paragraphs [0040] and [0041] as follows:

[0040] FIG. [[6]] 6A is a schematic cross-sectional view, which is taken along [[a]] line “VI-VI” of FIG. 5, and FIG. 6B is a schematic plane view showing an array layer of an organic electroluminescent device according to an embodiment of the present invention.

[0041] In FIG. [6] 6A, an organic electroluminescent device (ELD) 99 is fabricated by attaching first and second substrates 100 and 200 with a sealant 300. The first and second substrates 100 and 200 include a plurality of pixel regions “P” and dummy pixel regions “P<sub>D</sub>” surrounding the plurality of pixel regions “P.” A switching thin film transistor (TFT) T<sub>S</sub> (not shown in FIG. 6B) and a driving TFT “T<sub>D</sub>” are formed on an inner surface of the first substrate 100 adjacent to each pixel region “P.” Even though not shown in FIG. 6, a plurality of array lines are formed on the inner surface of the first substrate 100. A first electrode 202 is formed on

an inner surface of the second substrate 200. A sidewall 204 is formed on the first electrode 202 at a boundary of each pixel region “P” to surround each pixel region “P.” An organic electroluminescent layer 206 and a second electrode 208 are sequentially formed on the first electrode 202 in each pixel region “P.” An auxiliary pattern 203 may be formed between the first electrode 202 and the sidewall 204.

Please amend paragraph [0043] as follows:

[0043] As shown in [Fig. 6] FIG. 6A, for example, a dummy pixel region “ $P_D$ ” is disposed between the peripheral region where the pad 126 is formed and a pixel region “P.” The switching TFT (not shown), the driving TFT “ $T_D$ ” and the first connection electrode 130 are not formed on the portion of the first substrate 100 corresponding to the dummy pixel region “ $P_D$ .” As a result, the second electrode 208 in the dummy pixel region “ $P_D$ ” is electrically floating. Because of this arrangement, the organic EL diode can function normally even when the first electrode 202 contacts the second electrode 208 in the dummy pixel region “ $P_D$ ” due to a fabricating error.